

I2C-OC805SV2, I2C-OC805SAV2 I2C-Bus 8-Output Open Collectors

1 Features

- PCF8574 and PCF8574A I2C-bus I/O expander boards
- 8-output open collectors
- Operating voltage 2.5Vdc to 5.5Vdc
- Inverse polarity protection circuits on boards
- 100kHz I2C bus frequency
- Address by 3 jumpers for use of up to 8 addresses
- Up to 16 boards on a single bus (connected with PCF8574 and PCF8574A)
- Compatible with most microcontrollers
- Maximum load voltage 50Vdc
- 500mA rated collector current (using single output or summation of all outputs)
- PCB size 67x27mm

2 Description

This is the second version of an I2C bus output board for a remote 8-output expander via I2C bus based on PCF8574 and PCF8574A. It is called an I2C Output board. Making it ideal as an open collector expander for I2C-bus. They are designed to be compatible with most microcontrollers.

The PCF8574 and the PCF8574A are the I2C bus controllers which talk to microcontrollers and then take commands to output ports. They support 100kHz bus frequency. And they can be connected to the I2C port of most microcontrollers.

The boards use ULN2803 for an output driver. Each board has 8 open collectors. Each output supports 500mA when using single output. And the maximum voltage for loads is 50Vdc.

The board is addressed by 3 jumpers to make 8 different addresses. So that 8 boards can be connected on a single I2C-bus. Moreover 16 boards can be connected on a single bus when each board has a different address. By this way, 8 boards of the PCF8574 and 8 boards of the PCF8574A can be connected on a single bus. Because the PCF8574 and the PCF8574A have different addresses.

The boards have inverse polarity protection circuit for power supply input voltage to prevent damage from a mistake of connection.

The difference between the I2C-OC805SV2 and the I2C-OC805SAV2 is that the I2C-OC805SV2 uses PCF8574, but the I2C-OC805SAV2 uses PCF8574A.

Power dissipation of an ULN2803A chip is an importance for each application. So, it must be calculated for each application. Load current must not make a temperature of the chip rise over the limit of the power dissipation.



3 Diagram

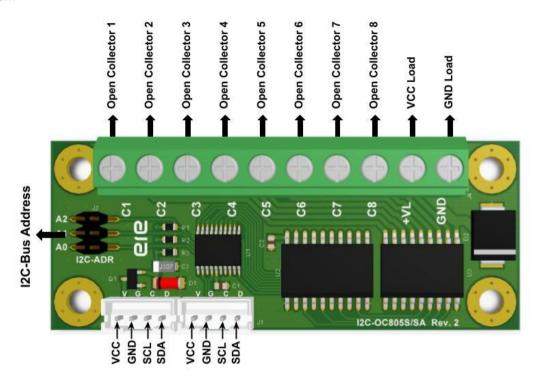


Figure 1: Board Diagram

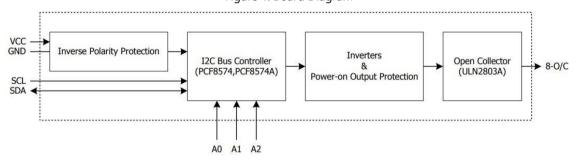


Figure 2: Block Diagram

Table 1: Pin Descriptions

Symbol	Description
VCC	Power supply input voltage 2.5Vdc to 5.5Vdc.
GND	Ground.
SCL	I2C bus serial clock signal.
SDA	I2C bus serial data signal.
A0	A jumper for selecting the address of A0.
A1	A jumper for selecting the address of A1.
A2	A jumper for selecting address of A2.
O/C1 O/C8	Open collector 1 to Open collector 8.
VCC LOAD	Power supply input voltage for load. 0 50V.
GND LOAD	Ground for Load.



4 Interfacing

The VCC and GND must be connected to VCC and GND respectively. Also, the SCL and SDA pins must be connected to SCL and SDA pins of microcontroller respectively.

I2C-OC805SV2 and I2C-OC805SAV2 can be connected from a board up to 16 boards on a single bus.

The I2C bus needs pull-up resistors for SCL and SDA lines. 10Kohm resistors are suitable for pull-up registers for 100KHz bus speed. These resistors can be connected externally.

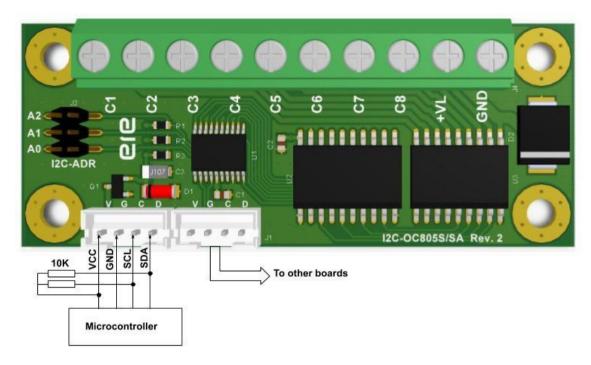


Figure 3: Microcontroller Interfacing

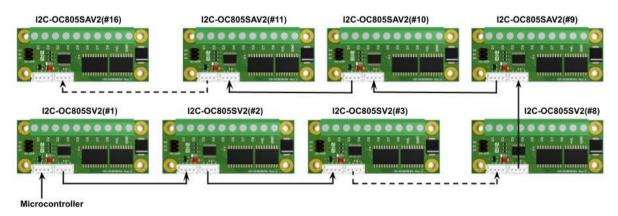
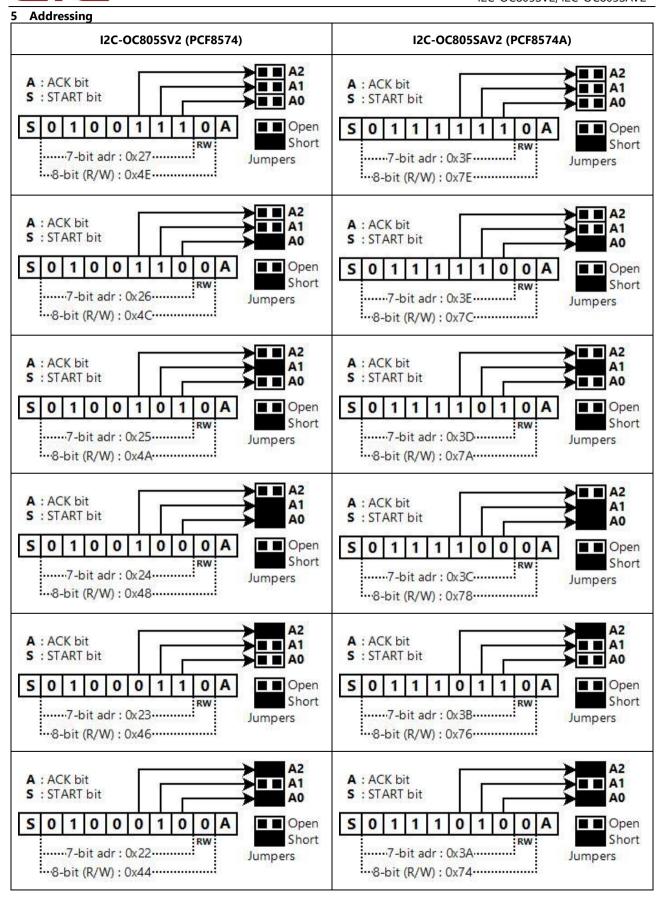


Table 14: Multiple Boards on a Single Bus







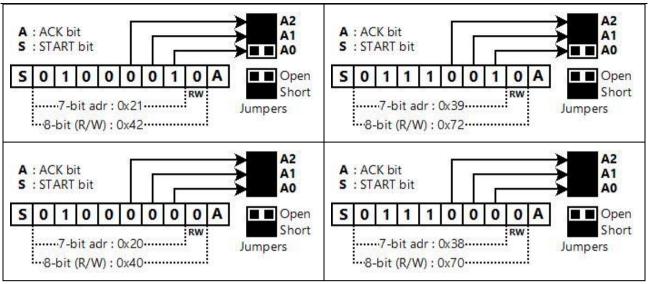


Figure 5: Addressing

6 Output data frame

When a microcontroller sends data to the output board via I2C bus. The first byte is an address byte. The second byte is data for outputs. The bit0 of the second byte controls O/C1. Bit value '1' means output is OFF and bit value '0' means output is ON.

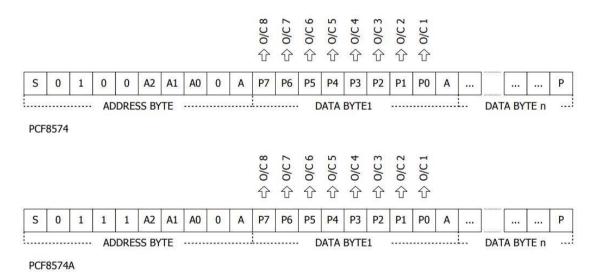


Figure 6: Output Data Frame of PCF8574 and PCF8574A

7 Load Connection

Boards have 8 outputs which can be connected to 8 loads. Each output is an open collector of a transistor that can support 500mA of load current and up to 50V. The resistive and inductive loads can be connected to the board directly because there are free-wheeling diodes on each driver. All free-wheeling diodes must be connected to VCC of loads. So, the VCC and GND of the loads must be connected to the board's terminals +VL and GND respectively.



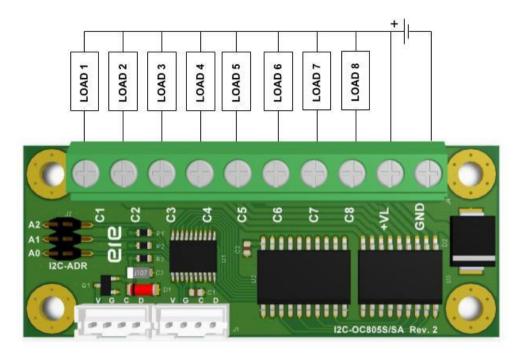


Figure 7: Load Connection

8 Specification

Table 2: Maximum Absolute Rating

Bus frequency	100kHz
Maximum board on bus	8(16) boards
Maximum load voltage	50V
Maximum load current	500mA (single output)
Output channels	8
Power supply input voltage	2.5Vdc – 5.5Vdc



9 Dimensions

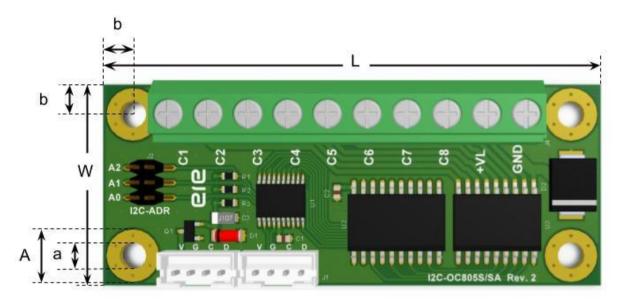


Figure 8: Dimensions

Table 3: Board dimensions

Units	Inch	mm
L	2.6378	67.00
W	1.0629	27.00
А	0.2795	7.10
а	0.1417	3.60
b	0.1574	4.00

10 Changing of Version2

Table 4: Changing of Version2

	Version2	Version1
LED	No (for reducing power consumption)	Yes
On-board pull-up resisters	No (for using external pull-up resisters)	Yes
Surge protection device	Yes (for more robust)	No
PCB	ENIG	HASL